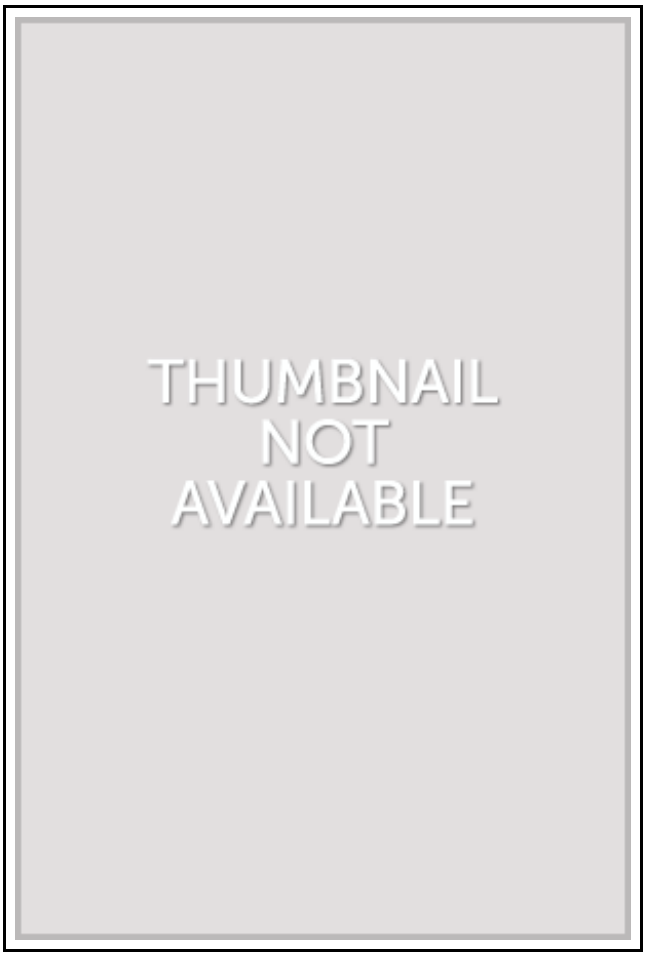
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THE DESIGNER S GUIDE TO THE CORTEX-M PROCESSOR FAMILY: A TUTORIAL APPROACH (PAPERBACK)

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ELSEVIER SCIENCE TECHNOLOGY, United Kingdom, 2013. Paperback. Book Condition: New. 234 x 192 mm. Language: English . Brand New Book. The Designer s Guide to the Cortex-M Family is a tutorial-based book giving the key concepts required to develop programs in C with a Cortex M- based processor. The book begins with an overview of the Cortex-M family, giving architectural descriptions supported with practical examples, enabling the engineer to easily develop basic C programs to run on the Cortex- M0/M0+/M3 and M4. It then examines the more advanced features of the Cortex architecture such as memory protection, operating modes and dual stack operation. Once a firm grounding in the Cortex M processor has been established the book introduces the use of a small footprint RTOS and the CMSIS DSP library. With this book you will learn: the key differences between the Cortex M0/M0+/M3 and M4; how to write C programs to run on Cortex-M based processors; how to make best use of the Coresight debug system; how to do RTOS development; the Cortex-M operating modes and memory protection; advanced software techniques that can be used on Cortex-M microcontrollers; how to optimise DSP code for the cortex M4 and how to build real time DSP systems; an Introduction to the Cortex microcontroller software interface standard (CMSIS), a common framework for all Cortex M- based microcontrollers; coverage of the CMSIS DSP library for Cortex M3 and M4; and an evaluation tool chain IDE and debugger which allows the accompanying example projects to be run in simulation on the PC or on low cost hardware.

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